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TILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
12/11/2001	Kouichi Harada	09792909-5276	6112	
05/05/2005		EXAM	INER	
SONNENSCHEIN NATH & ROSENTHAL LLP			HANNETT, JAMES M	
WACKER DRIVE STATION, SEARS TOWER CHICAGO, IL 60606-1080		ART UNIT	PAPER NUMBER	
		2612		
	05/05/2005 NATH & ROSEN FATION, SEARS T	05/05/2005 NATH & ROSENTHAL LLP FATION, SEARS TOWER	05/05/2005 EXAM NATH & ROSENTHAL LLP HANNETT TATION, SEARS TOWER ART UNIT	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
Office Action Summary	10/015,160	HARADA, KOUICHI			
• • • • • • • • • • • • • • • • • • •	Examiner	Art Unit			
The MAILING DATE of this communication	James M. Hannett	2612			
Period for Reply	appears on the cover sheet with the t	correspondence address			
A SHORTENED STATUTORY PERIOD FOR RETHE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication. If the period for reply specified above is less than thirty (30) days, and if NO period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by some and patent term adjustment. See 37 CFR 1.704(b).	ON. R 1.136(a). In no event, however, may a reply be tinn. reply within the statutory minimum of thirty (30) day ariod will apply and will expire SIX (6) MONTHS from tatute, cause the application to become ABANDONE	mely filed ys will be considered timely. the mailing date of this communication. ED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 1	11 December 2001.				
_					
3) Since this application is in condition for all					
Disposition of Claims					
4) ⊠ Claim(s) <u>1-5</u> is/are pending in the applicating 4a) Of the above claim(s) is/are with 5) ⊠ Claim(s) <u>1-4</u> is/are allowed. 6) ⊠ Claim(s) <u>5</u> is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and	drawn from consideration.	•			
Application Papers					
9) The specification is objected to by the Exam 10) The drawing(s) filed on 11 December 2001 Applicant may not request that any objection to Replacement drawing sheet(s) including the co	is/are: a)⊠ accepted or b)⊡ object the drawing(s) be held in abeyance. Se rrection is required if the drawing(s) is ob	ee 37 CFR 1.85(a). ojected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of: 1. Certified copies of the priority docum 2. Certified copies of the priority docum 3. Copies of the certified copies of the application from the International But * See the attached detailed Office action for a	nents have been received. nents have been received in Applicat priority documents have been receiv rreau (PCT Rule 17.2(a)).	tion No red in this National Stage			
Attachment(s) 1) Notice of References Cited (PTO-892)	4) Interview Summan	v (PTO-413)			
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)					
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SI Paper No(s)/Mail Date		Patent Application (PTO-152)			

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DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 1: Claim 5 is rejected under 35 U.S.C. 102(b) as being anticipated by USPN 4,012,587 Ochi et al.
- 2: As for Claim 5, Ochi et al teaches on Column 3, Lines 18-30 and depicts in Figure 2A a method of driving a solid state image pickup device (1) comprising a plurality of two-dimensionally arranged photoelectric conversion elements (2), gate portions (6) for reading out signal charges photo-electrically converted in the plural photoelectric conversion elements (2), a plural vertical transfer registers (3) for transferring the signal charges read out by the gate portions (6) in the vertical direction. Ochi et al teaches on Column 3, Lines 18-30 that each vertical shift register is a two-phase driven vertical shift register driven by electrodes Φ1 and Φ2. Furthermore, the two-phase signal is a pulse that alternates between a high voltage and a low voltage, the examiner views the first driving voltage as the voltage when the driving signal is high and the second voltage as the voltage when the driving signal is low. Therefore both of these voltage values are applied to both electrodes Φ1 and Φ2 alternatively for transferring the signal charges in the vertical direction. Ochi et al teaches on Column 1, lines 33-40 the read-out operation of the signal charges which are photo-electrically converted by the photoelectric conversion elements (2) to the vertical transfer registers (3) is carried out on the plural

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photoelectric conversion elements (2) independently every line by the driving voltages (two phase driven clock) applied to both the first (Φ 1) and second (Φ 2) transfer electrodes. Ochi et al teaches that the image data for every pixel in a line is read out and does not teach that he image data is mixed and then read out. Therefore, Ochi et al teaches that the image data is read out independently for every line of image data.

Allowable Subject Matter

3: Claims 1-4 are allowed.

The following is a statement of reasons for the indication of allowable subject matter: the prior art teaches the use of an image sensor with a plurality of photoelectric conversion elements; gate portions for reading out the signals from the pixels; a plurality of vertical transfer registers; first transfer electrodes and second transfer electrodes. However, the prior art does not teach the detailed electrode and pixel layout structure as defined in Claim 1.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. USPN 4,603,343 Matsumoto et al teaches and depicts in Figure 1 the use of an image sensor with a plurality of vertical shift registers driven by a plurality of voltage signals; USPN 4,989,095 Whitesal et al teaches the use of a CCD image sensor which utilizes a horizontal shift register, a plurality of vertical shift registers and a plurality of voltage signals to drive the vertical shift registers; USPN 4,837,630 Ueda teaches the use of a CCD image sensor which utilizes a horizontal shift register, a plurality of vertical shift registers and a plurality of voltage signals to drive the vertical shift registers.

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Any inquiry concerning this communication or earlier communications from the

examiner should be directed to James M. Hannett whose telephone number is 571-272-7309.

The examiner can normally be reached on 8:00 am to 5:00 pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Wendy Garber can be reached on 571-272-7308. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James M. Hannett

Examiner

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JMH

April 26, 2005

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